

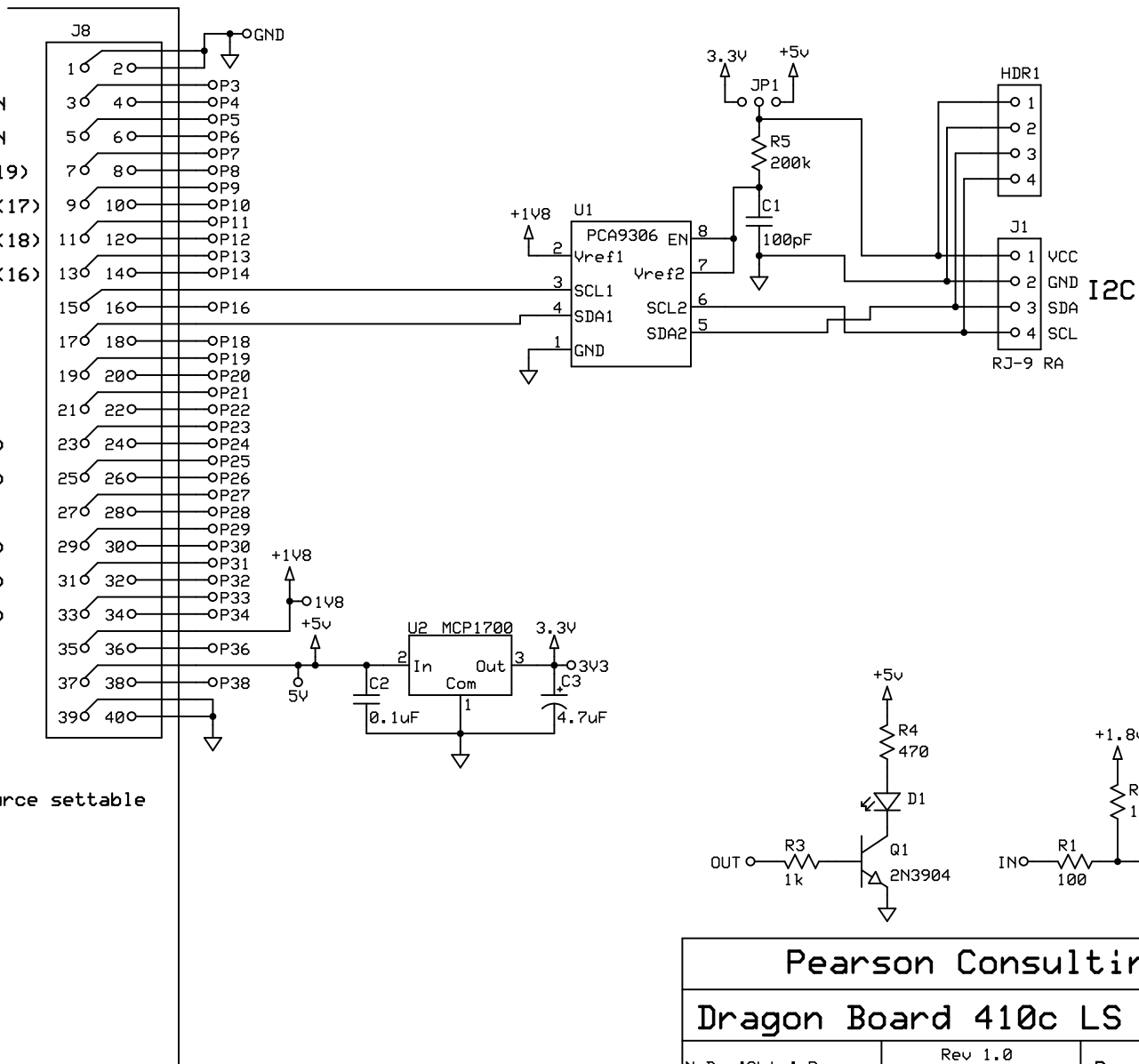
Dragon Board 410c

NOTE: APQ GPIO_< >

GND	GND
UART0_CST_N<2>	PHONE_ON_N
UART0_TX<0>	PM_RESIN_N
UART0_RX<1>	SPI0_CLK<19>
UART0_RTS_N<3>	SPI0_MISO<17>
UART1_TX<4>	SPI0_CS_N<18>
UART1_RX<5>	SPI0_MOSI<16>
I2C0_SCL<7>	GPIO_110
I2C0_SDA<6>	GPIO_113
I2C1_SCL<23>	GPIO_114
I2C1_SDA<22>	N.C.
GPIO_A<36>	GPIO_B<12>
GPIO_C<13>	GPIO_D<69>
GPIO_E<115>	GPIO_F
GPIO_G<24>	GPIO_H<25>
GPIO_I<35>	GPIO_J<34>
GPIO_K<28>	GPIO_L<33>
+1V8	SYS_DCIN
+5V	SYS_DCIN
GND	GND

NOTE: I/O pins are 1.8v

2ma to 8ma sink/source settable



Pearson Consulting

Dragon Board 410c LS Header

N.D. 'Chip' Pearson

Rev 1.0
3/31/2017

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